

**Amendments to the Specification:**

Please replace the title as follows:

~~NOISE CONTROL CIRCUIT~~

NOISE SUPPRESSION CIRCUIT

Please replace the paragraph beginning on page 46, line 23, with the following rewritten paragraph:

The action of the noise suppression circuit according to the second embodiment will now be described on the basis of the configuration example of Fig. 9A. First, as shown in Fig. ~~4A~~ 9A, the case where the voltage  $V_i$  in the normal mode is applied across the terminals 1a and 1b will be described. In this case, the voltage  $V_i$  is applied across one end of the first inductor 51 and one end of the fourth inductor 54. The voltage  $V_i$  is divided by the first inductor 51, the series circuit 15, and the fourth inductor 54, and predetermined voltages in the same direction are generated between both ends of the first inductor 51, across both ends of the series circuit 15, and across both ends of the fourth inductor 54. Each of the arrows in the diagram shows that the potential at the pointing side is higher.